

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/926,377	GRASSL, THOMAS	
Examiner	Art Unit		Page 1 of 1
David L. Hogans	2813		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,426,072	06-1995	Finnila, Ronald M.	438/107
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era (2000), Lattice Press, Volume 1, Second Edition, pages 268-269.
	V	Quirk et al., Semiconductor Manufacturing Technology (2001), Prentice Hall, page 301.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.